

# Notice of Allowability

Application No.

10/643,268

Examiner

Nam T Nguyen

Applicant(s)

PENNEY, DANIEL B.

Art Unit

2824

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☐ This communication is responsive to \_\_\_\_\_.
2. ☒ The allowed claim(s) is/are 44-79.
3. ☒ The drawings filed on 05 November 2001 are accepted by the Examiner.
4. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) ☐ All b) ☐ Some\* c) ☐ None of the:
    1. ☐ Certified copies of the priority documents have been received.
    2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
    3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

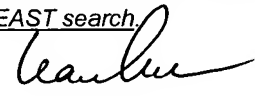
Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  
**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
  - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
    - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
  - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

## Attachment(s)

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☒ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date 2/27/04
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413), Paper No./Mail Date \_\_\_\_\_
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☒ Other EAST search

  
Vu A. Le  
Primary Examiner

### **DETAILED ACTION**

1. Claims 44-79 are pending in the application.

### ***Information Disclosure Statement***

2. The information disclosure statement (IDS) submitted on **8/18/03** was filed same with the mailing date of the present application. The submission is in compliance with the provisions of 37 CFR 1.97. Accordingly, the information disclosure statement is being considered by the examiner.

### **REASONS FOR ALLOWANCE**

3. Claims 44-79 are allowed.

The following is an examiner's statement of reasons for allowance:

There is no teaching or suggestion in the prior art to:

“ in response to the external memory address having an odd value, providing the predecode address in the predecode sequence as the first internal memory address, changing the predecode address into a modified sequence, and providing the modified predecode address as the second internal memory address” as claimed in the independent claim 44; or

“ based on the value of the external memory address, applying the predecode signals as the second of the pair of internal burst addresses in response to the external memory address having a first value, and in response to the external memory address

Art Unit: 2824

having a second value, rerouting the predecode signals into a reordered arrangement to be applied as the second of the pair of internal burst addresses” as claimed in the independent claim 52; or

“ reordering the sequence of bits of the predecoded external memory address and providing the reordered predecoded external memory address as the second internal memory address in response to an odd valued external memory address” as claimed in the independent claim 59; or

“ in response to the least significant bit (LSB) of the external memory address is equal to zero, applying the  $2^{**N}$  bit predecode address as a second internal memory address for accessing a second memory array, and in response to the LSB of the external memory address is equal to one, reordering the sequence of bits of the  $2^{**N}$  bit predecode address to a modified sequence of  $2^{**N}$  bits and applying the modified  $2^{**N}$  bits as the second internal memory address” as claimed in the independent claim 65; or

“ in response to the external memory address being an odd value, providing the predecoded address as the first internal memory address, reordering the bit sequence of the predecoded address and providing the reordered predecoded address as the second internal memory address, the predecoded address and the reordered predecoded address sequential in value” as claimed in the independent claim 72.

4. The following prior art, which is considered pertinent to applicant's disclosure although not relied upon, includes:

Tomita (US Pat. No. 5,881,009) or Iwanari et al. (US Pat. No. 5,841,727) discloses precode signal circuit similar to that of the present application, but fail to disclose as described above.

5. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nam T Nguyen whose telephone number is (571) 272-1878. The examiner can normally be reached on 8 am to 5:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard Elms can be reached on (571) 272-1869. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Application/Control Number: 10/643,268  
Art Unit: 2824

Page 5

2/27/04  
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Nam T Nguyen  
Examiner  
Art Unit 2824

A handwritten signature in black ink, appearing to read 'Vu A. Le', written in a cursive style.

Vu A. Le  
Primary Examiner